

Sriharsha Dokula

Columbus, OH | (607) 595 7798 | sdokula1@binghamton.edu | [Linkedin](#) | [Github](#)

PROFILE SUMMARY

Experienced Software Developer with 3.5+ years of expertise in automation development using REST APIs, Python, and batch scripting. Skilled in designing and implementing efficient automation solutions, with additional knowledge of Privileged Access Management (PAM) solutions and security. Demonstrates a strong ability to learn quickly and adapt to new technologies.

PROFESSIONAL EXPERIENCE

SUNY Binghamton, *Research Assistant* | NY, USA

July 2024 - Present

- Assisted in comprehensive **literature review**, meticulously gathering and **analyzing relevant information**.
- Conducted detailed and extensive code reviews across multiple programming languages and design styles to understand engineering requirements, utilizing tools like **Git** and **VS Code**.
- Contributing to the development and enhancement of the **Tensor-Aware Energy Accounting** framework for machine learning.

Cognizant, *Software Developer* | Bengaluru, India

July 2019 – December 2021

- Spearheaded the design, development, and testing of automation scripts using **Python, batch scripting, and REST APIs**, significantly reducing manual workload by 85% and improving operational efficiency in Privileged Identity Management (PIM) solutions.
- Automated the deployment of CyberArk's PVWA component using **Ansible playbooks**, streamlining deployment processes and reducing setup time.
- Implemented automated workflows utilizing CyberArk's APIs, replacing 14 manual maintenance workflows, resulting in significant time and cost savings.
- Conceptualized and executed innovative **Proof of Concepts (POCs)** on CyberArk components, including Enterprise Password Vault (EPV), Application Access Manager (AAM), and Privileged Threat Analytics (PTA), validating their effectiveness and suitability within the organizational context, which eventually led to the adaptation of AAM.
- Utilized Splunk integration for automatic monitoring and automation of daily onboarding of users and accounts to CyberArk, enhancing operational workflows and reducing manual intervention.
- Authored comprehensive **documentation** outlining post-production technical support procedures, contributing to a 95% decrease in resolution time for post-production technical issues.
- Mentored** and trained over 100 interns on CyberArk, ensuring smooth onboarding and fostering a culture of continuous learning and professional development.
- Acted as a key liaison between cybersecurity and other functional teams, facilitating effective communication and collaboration to achieve IAM project objectives, with a particular emphasis on CyberArk integration and optimization

Cognizant, *Program Analyst* | Bengaluru, India

January 2019 – June 2019

- Developed automation solutions using **python**, optimizing operational efficiency, and reducing manual effort.
- Implemented **automated reporting** tool, to detect vulnerable computers, providing visualized data for actionable insights across a network of over 10000 endpoints.

TECHNICAL SKILLS

Languages: Python, C++, C, Java, JavaScript

Databases: SQL, PL/SQL, MongoDB

Software and OS: Visual Studio Code, Jupyter Notebook, Git, Linux, Vim

Additional: PowerShell, batch scripting, Ansible, Docker, AWS, HTML, CSS, XML Parsing, TCP/IP, SQL Workbench

IAM tools: CyberArk (Vault, CPM, PSM, PVWA, AAM) and Okta

Certifications: CompTIA Security + (certified in 2018), CyberArk Certified Trustee, GitHub Foundations.

EDUCATION

Binghamton University, State University of New York, Thomas J. Watson College of Engineering and Applied Science

Master of Science in Computer Science - (CGPA: 3.9 /4.00)

January 2023 - May 2024

Relevant Coursework: Design & Analysis Computer Algorithm, Computer Architecture & Organizations, Operating Systems, Programming Languages, Database Systems, Hardware and Security Systems, Design Patterns

PROJECT EXPERIENCE

Out-of-Order CPU simulation, *c Developer*

February 2023

- Developed a simulation of an Out-of-Order CPU with a 9-stage pipeline, incorporating advanced features like register renaming and multiple functional units.

Xv6 Shared Memory pages, *c Developer*

November 2023

- Implemented dynamically allocated shared memory pages, allowing for flexible and scalable memory management within the xv6 environment, thereby enhancing system performance and resource utilization.

REFERENCE AVAILABLE ON REQUEST

- Reference letter available upon request from Professor Yu (David) Liu, Programming Language course instructor and research supervisor at SUNY Binghamton.